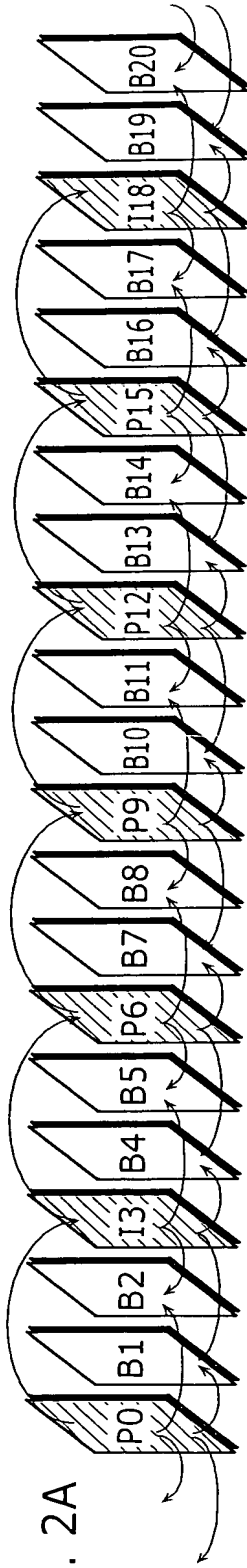


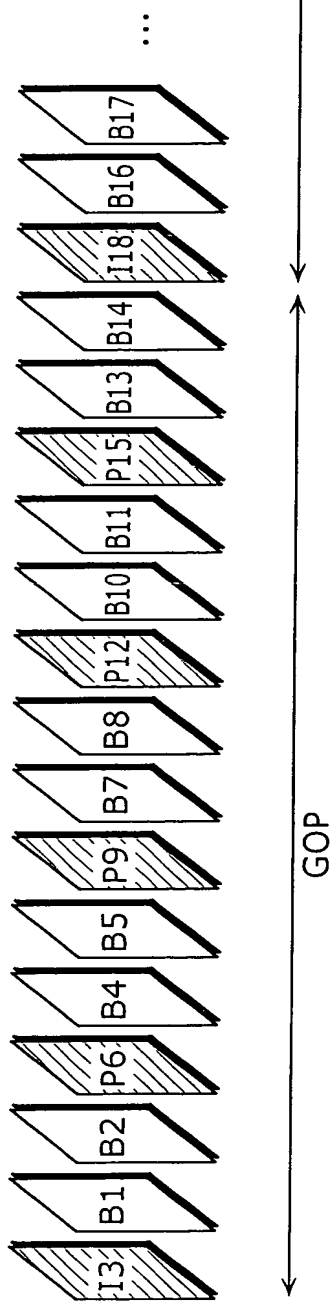
Display order

FIG. 2A



Decoding order

FIG. 2B



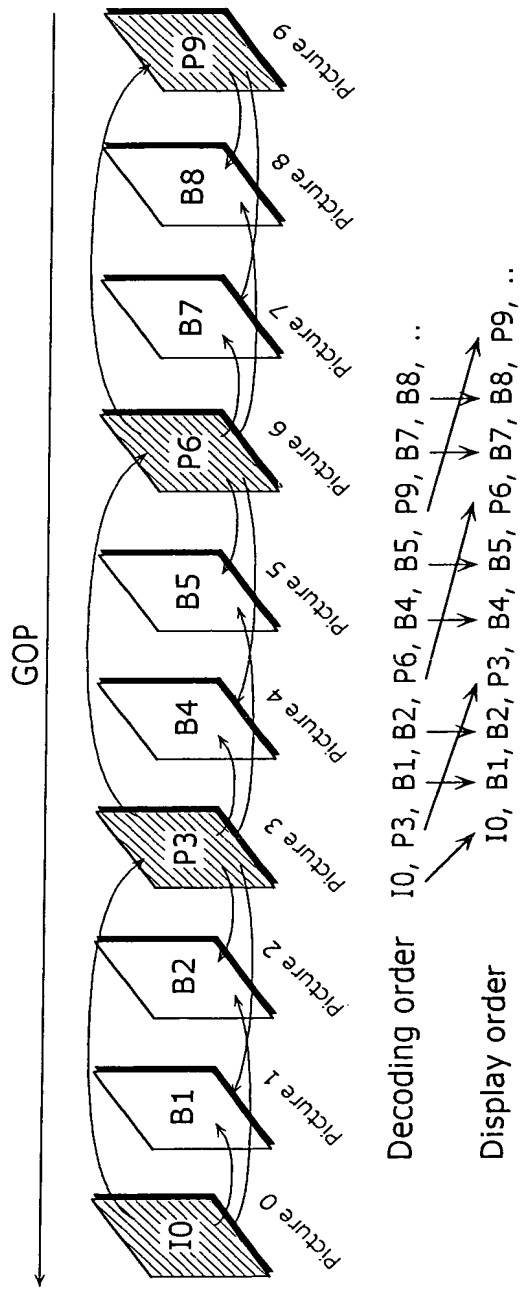


FIG. 3A

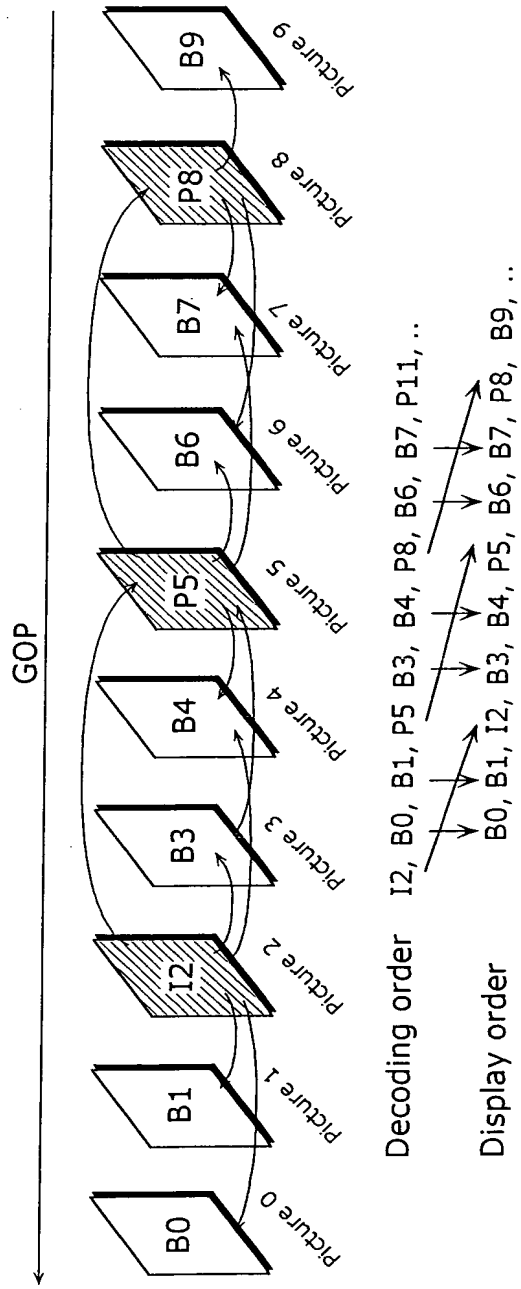


FIG. 3B

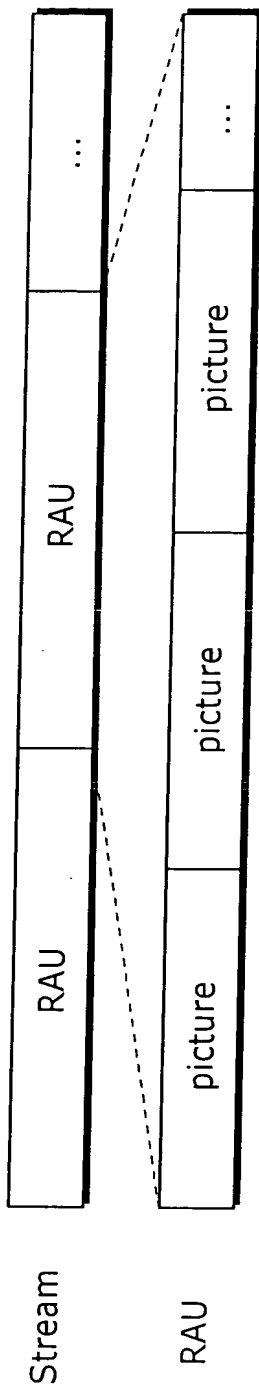


FIG. 4

FIG. 5

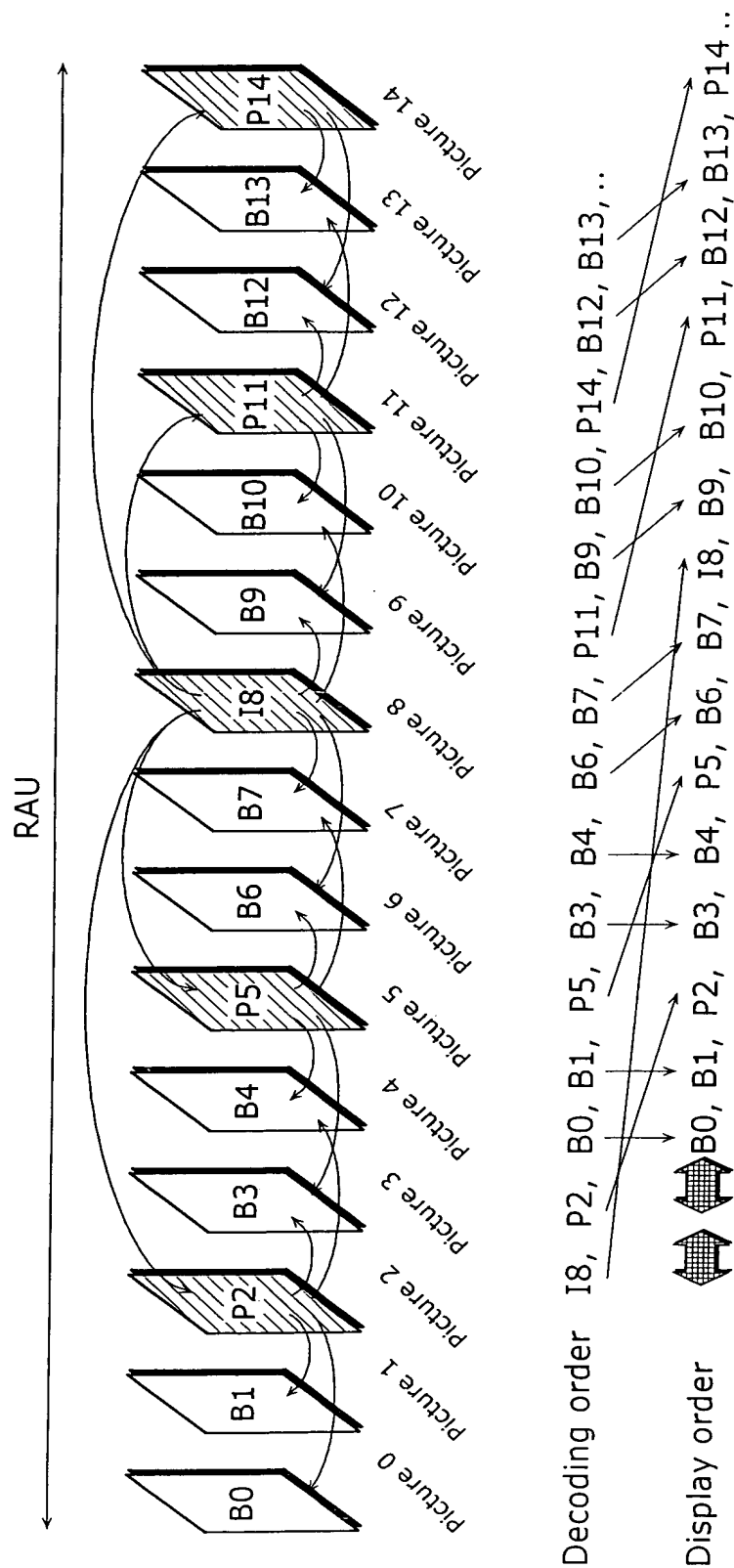


FIG. 6B Stream 2

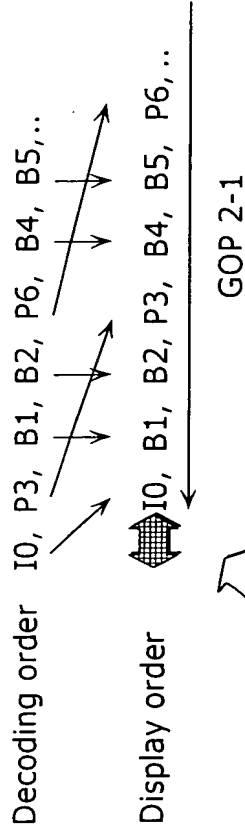


FIG. 6A Stream 1

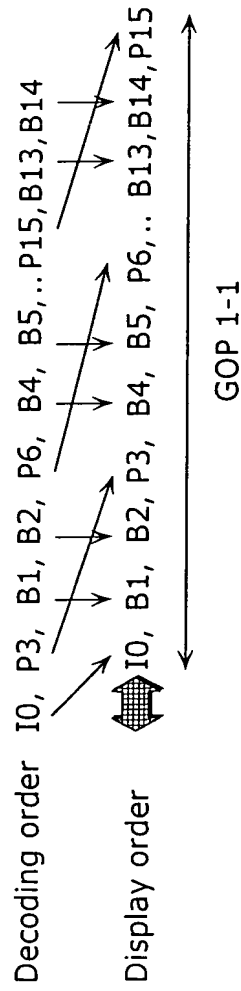


FIG. 6C Stream 3

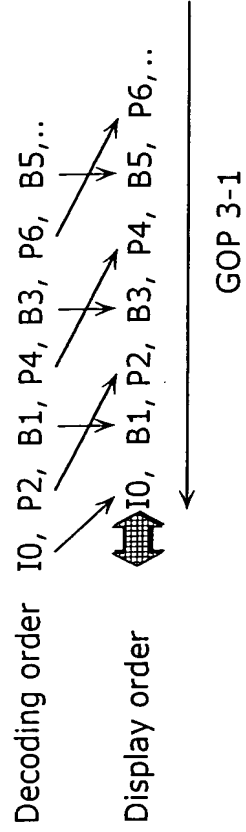


FIG. 7

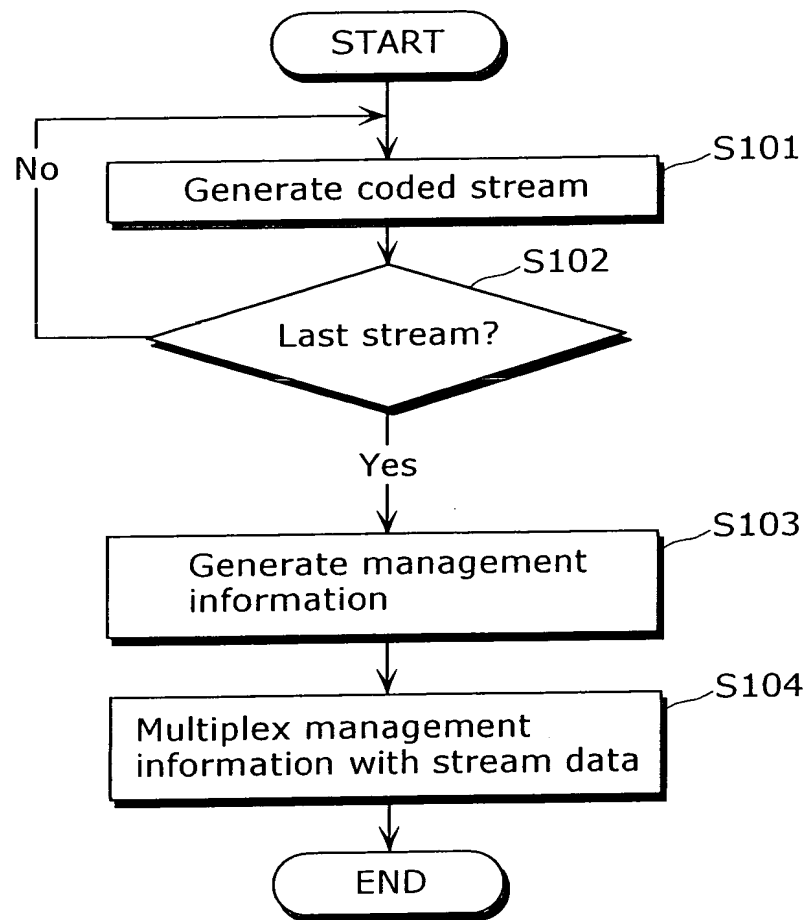


FIG. 8

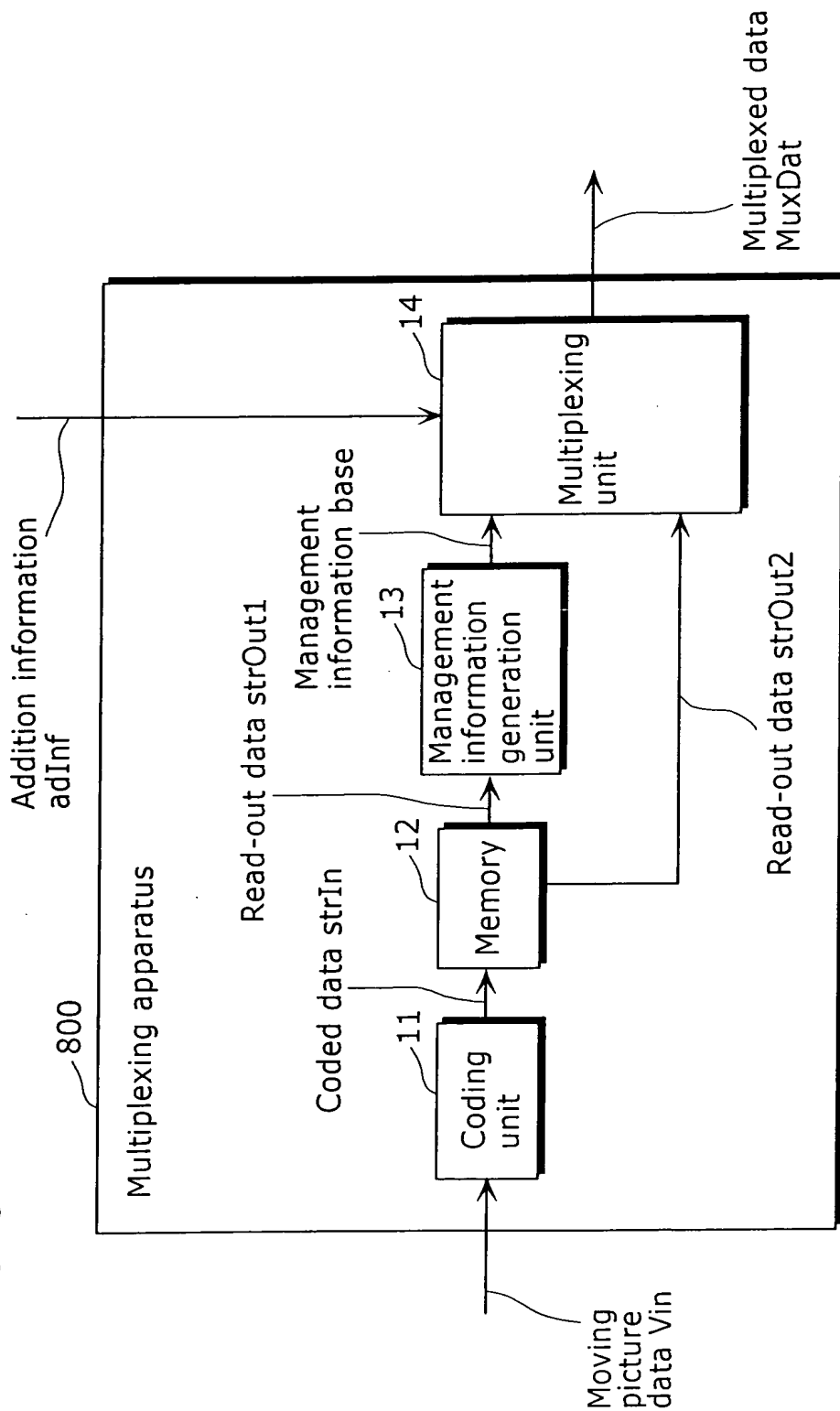


FIG. 9A

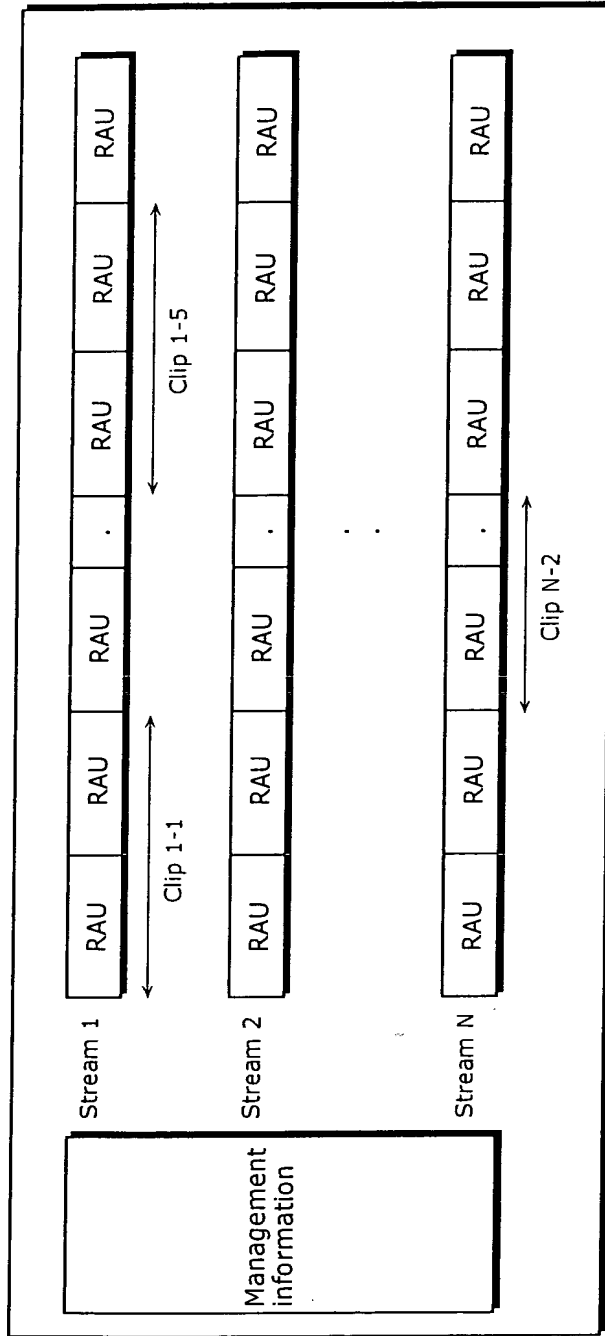


FIG. 9B

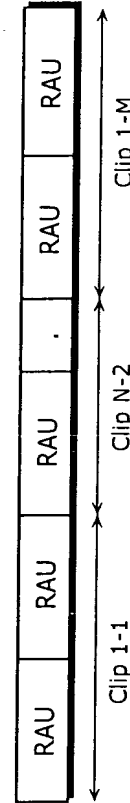


FIG. 9C

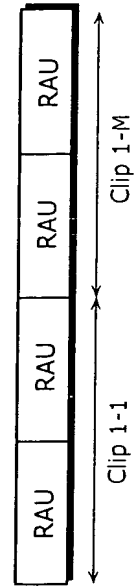


FIG. 10

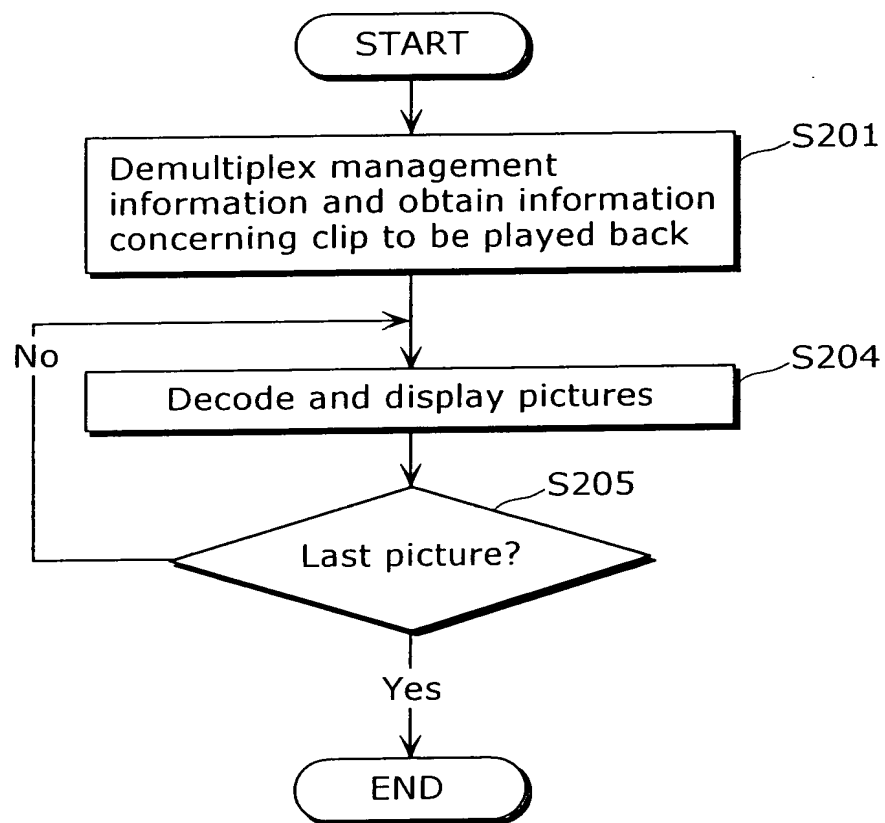


FIG. 11

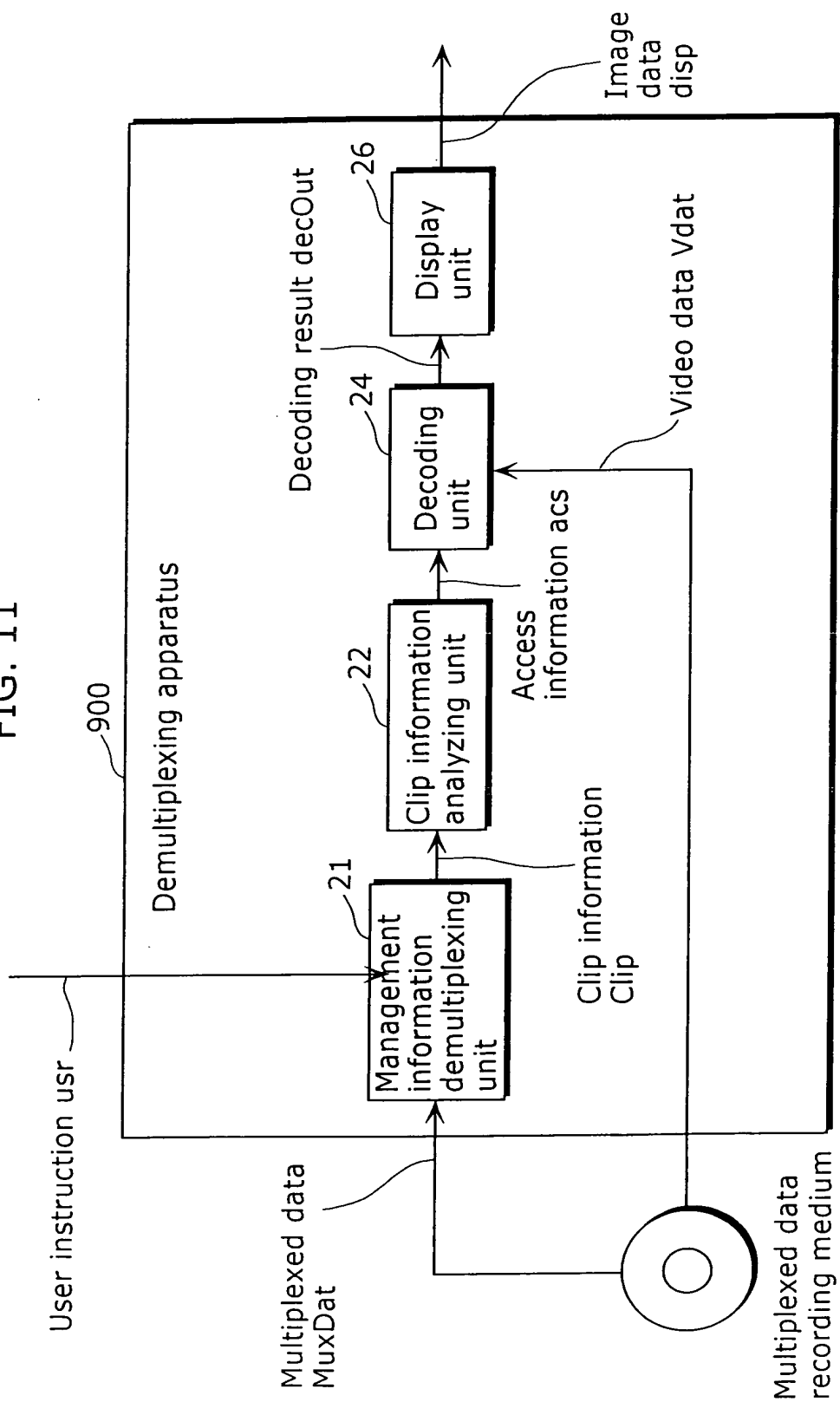


FIG. 12B Stream 2

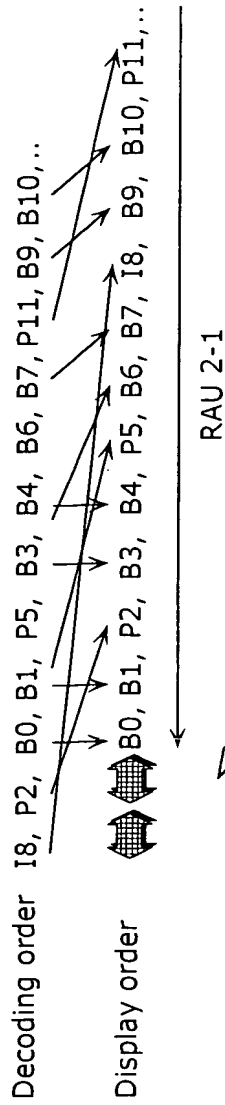


FIG. 12A Stream 1

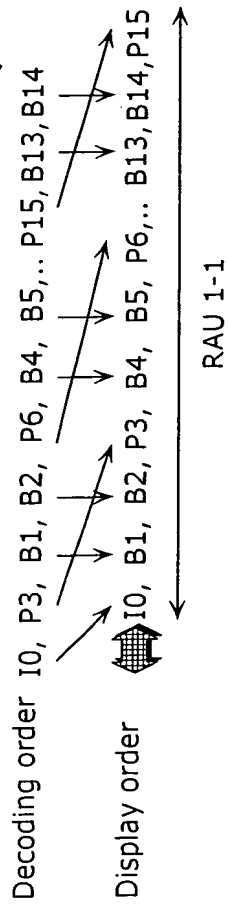


FIG. 12C

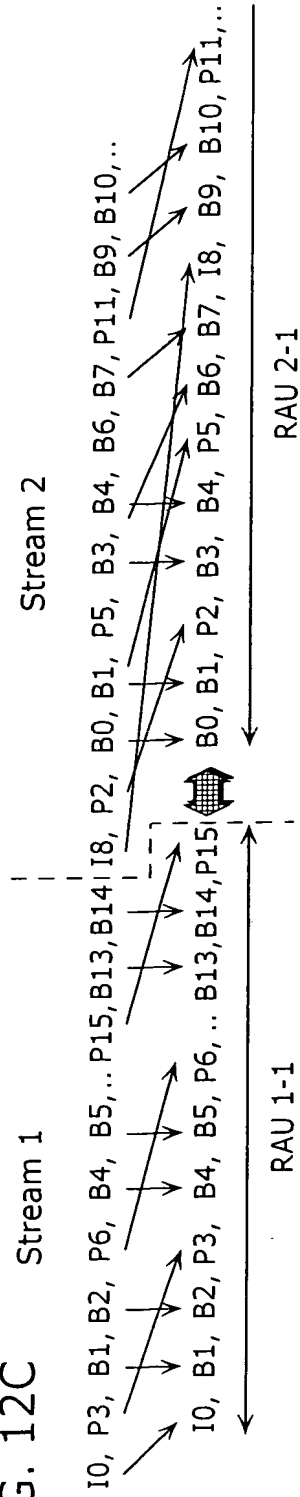


FIG. 13

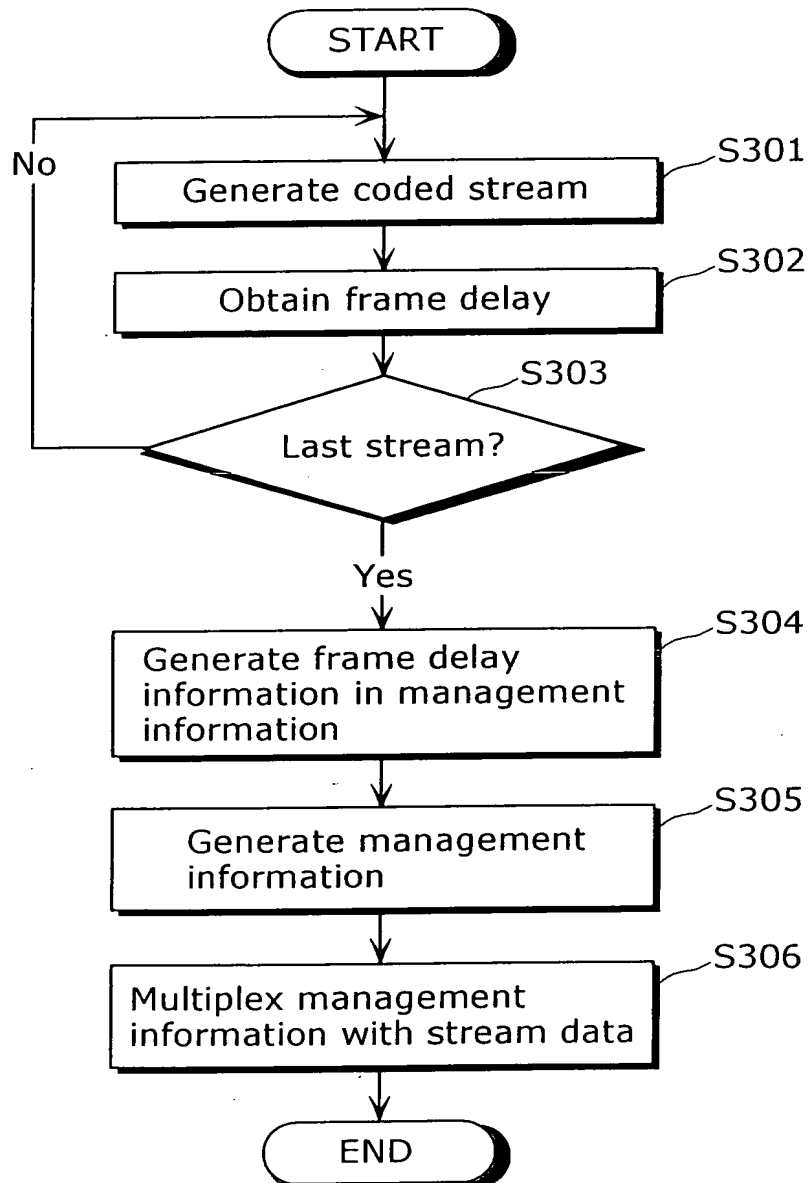
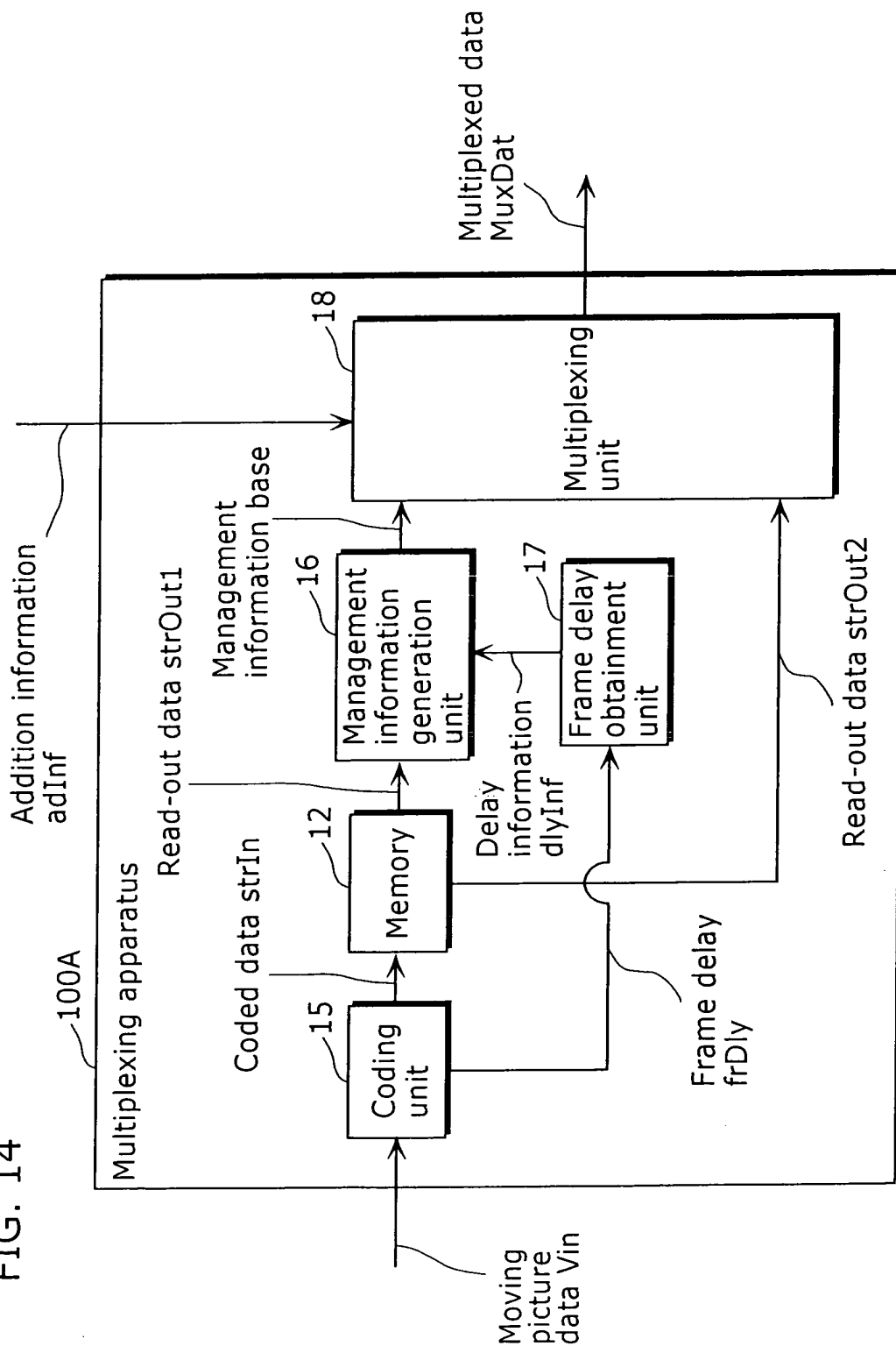


FIG. 14



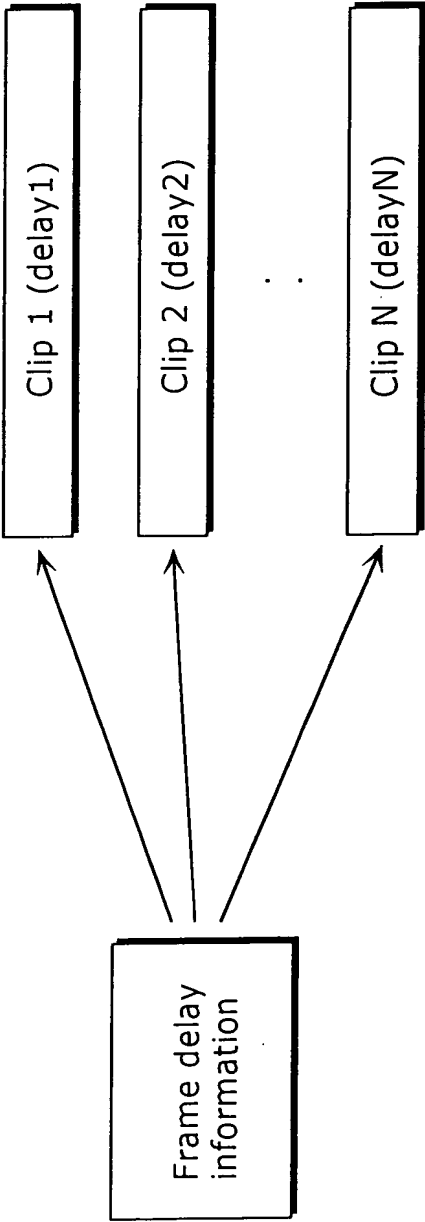


FIG. 15A

Delay table

delay1
delay2
..
delayN

FIG. 15B

FIG. 15D Value of frame delay (Common)

FIG. 15E Information indicating whether frame delays are equal among clips

FIG. 15C Maximum value of answer obtained from delay1-N

FIG. 16

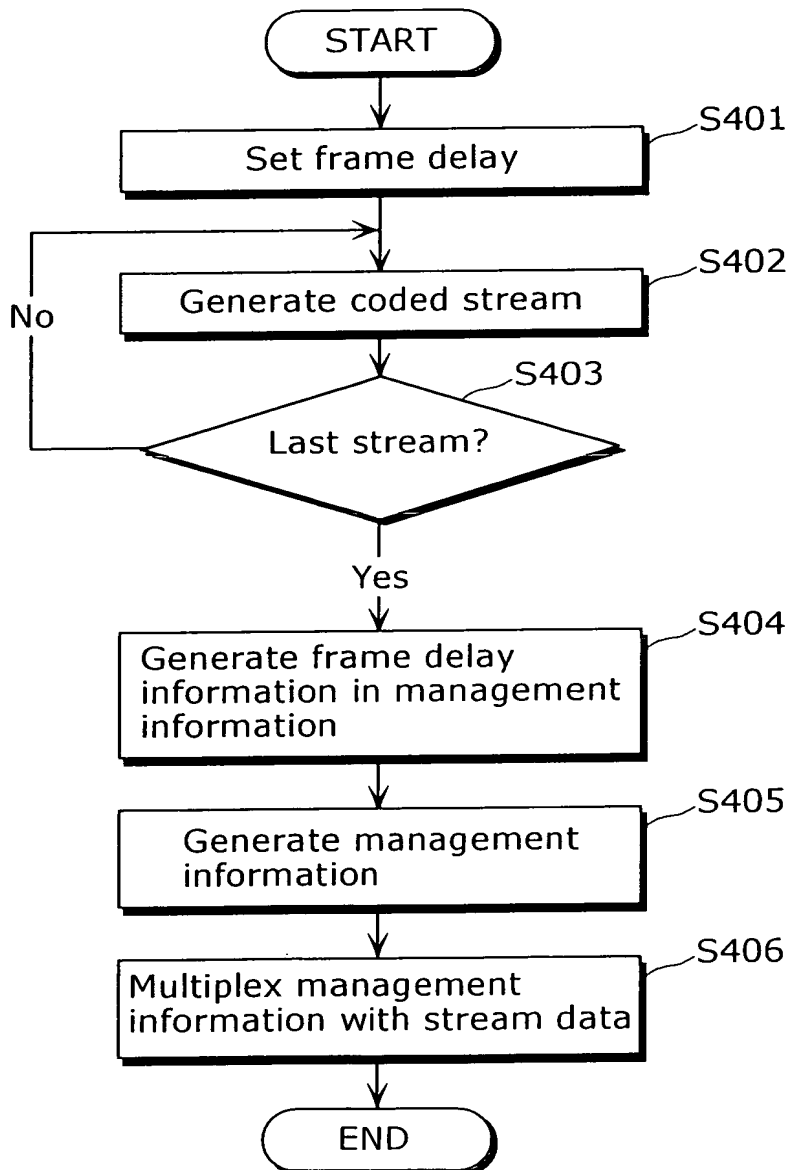


FIG. 17

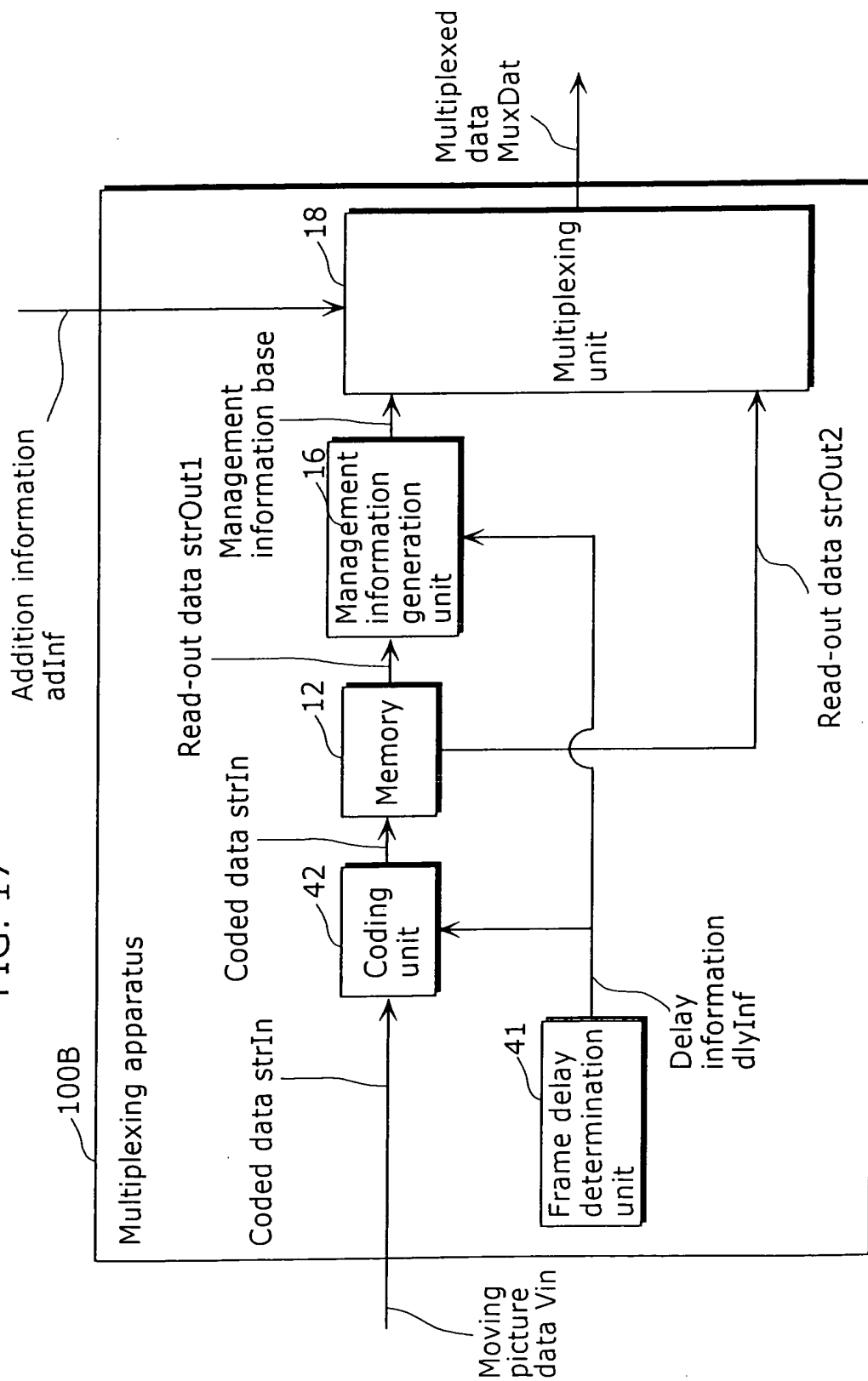


FIG. 18

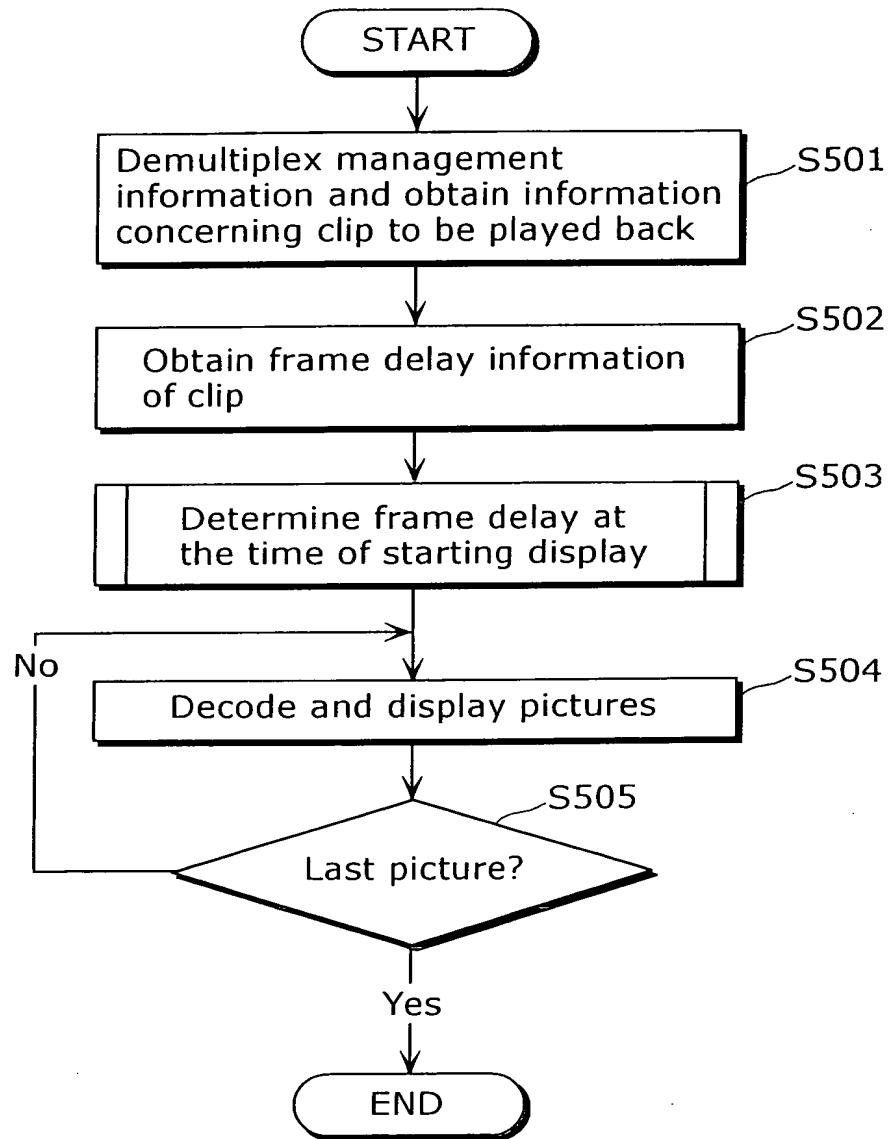


FIG. 19

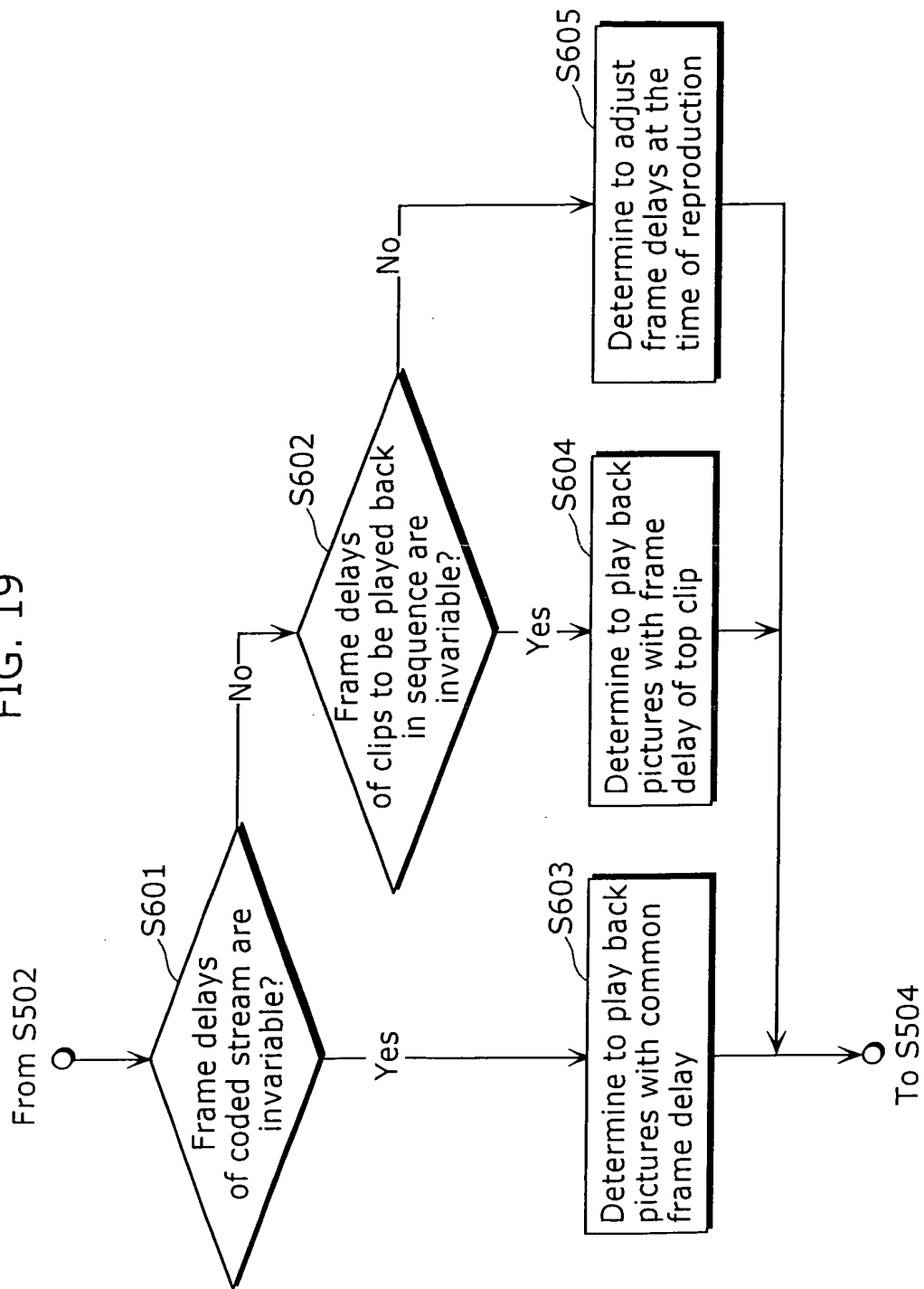


FIG. 20A Clip 1

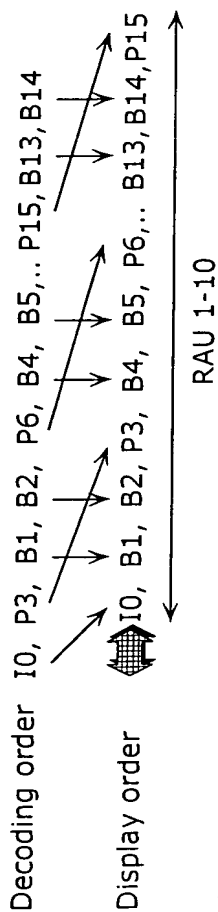


FIG. 20B Clip 2

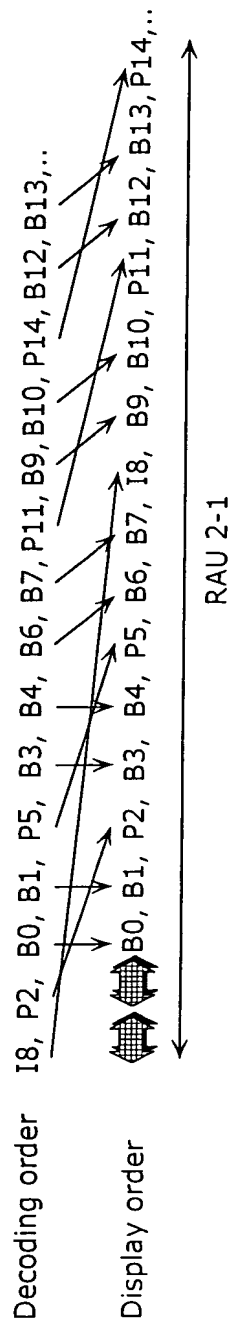


FIG. 20C

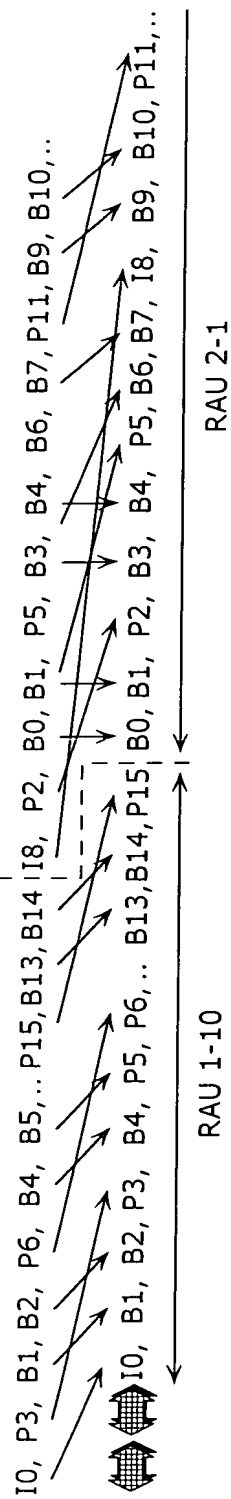


FIG. 20D

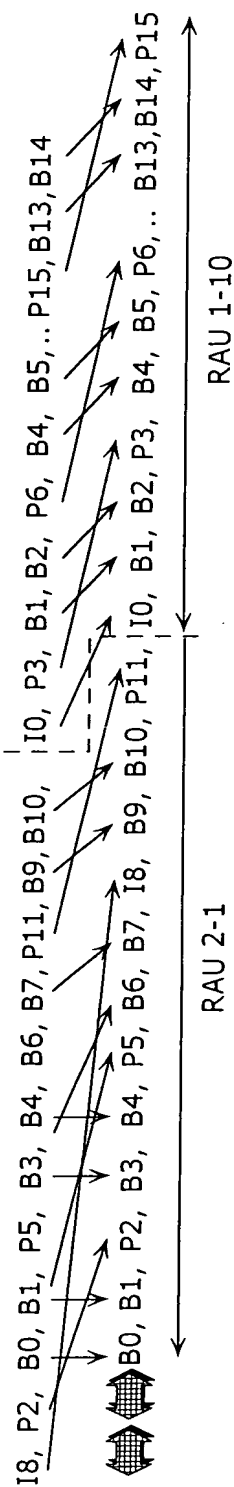


FIG. 21

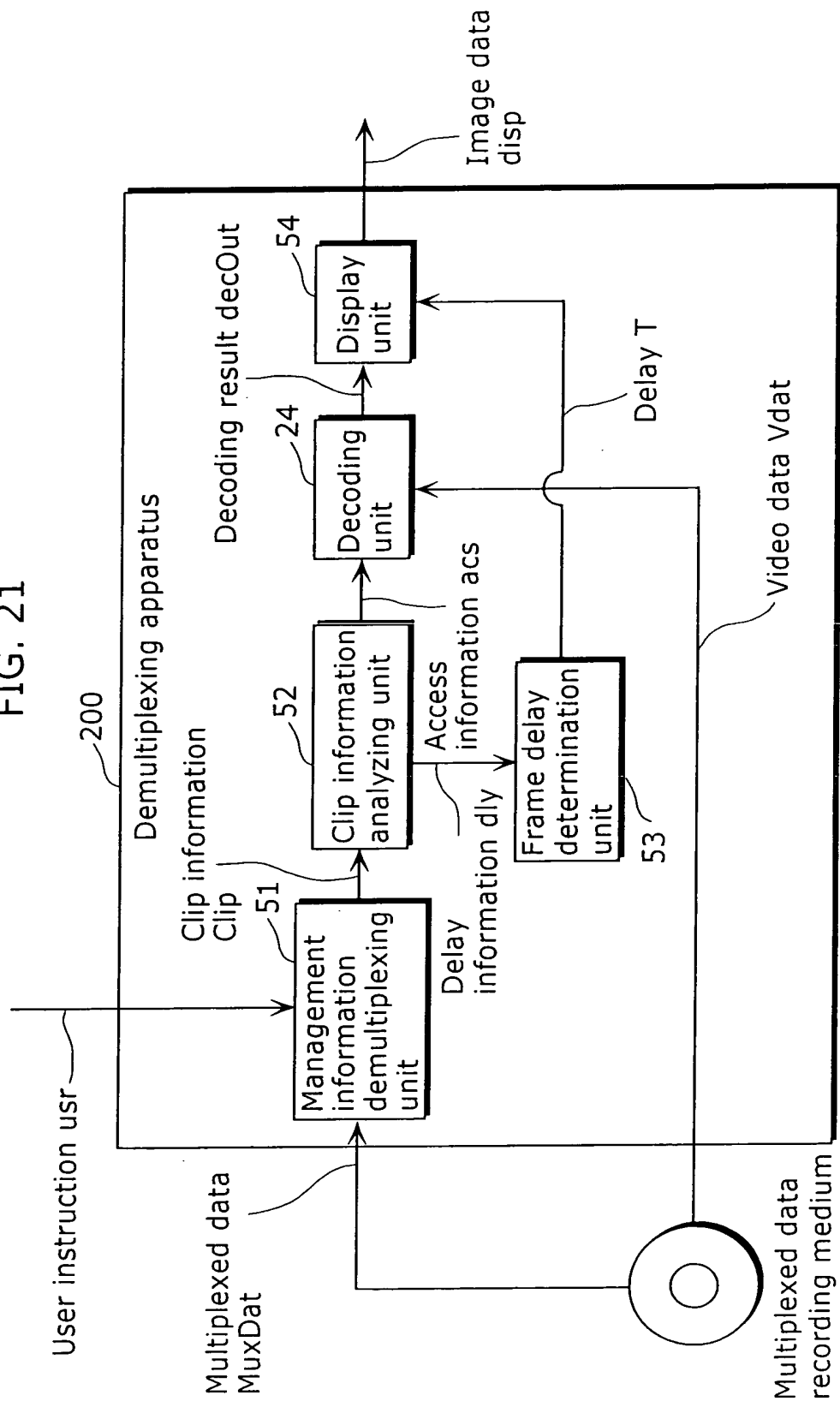


FIG. 22

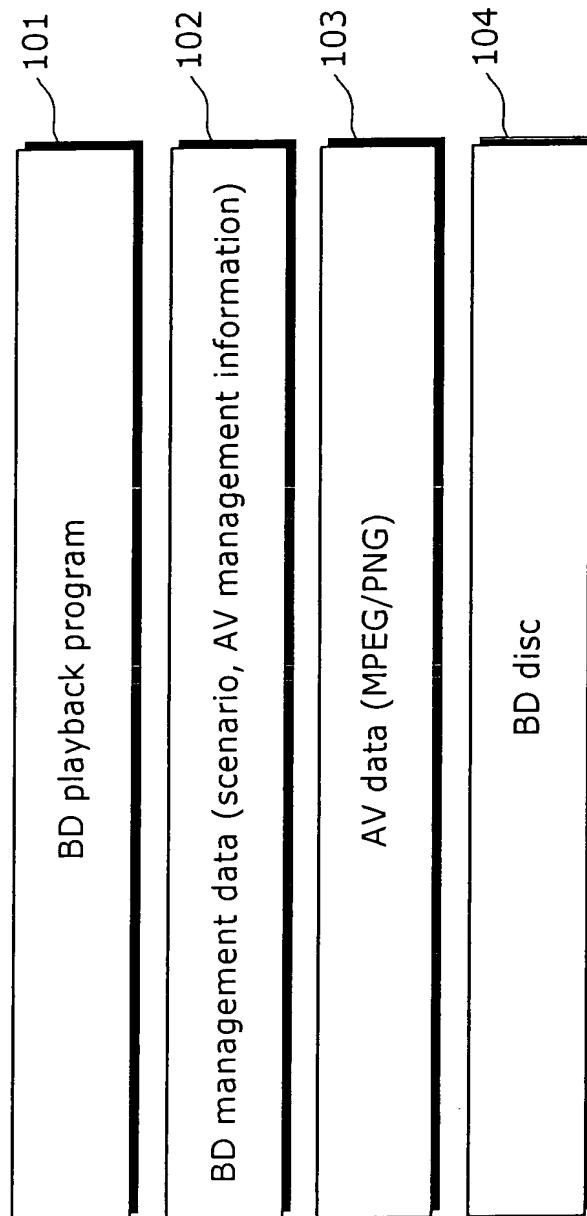


FIG. 23

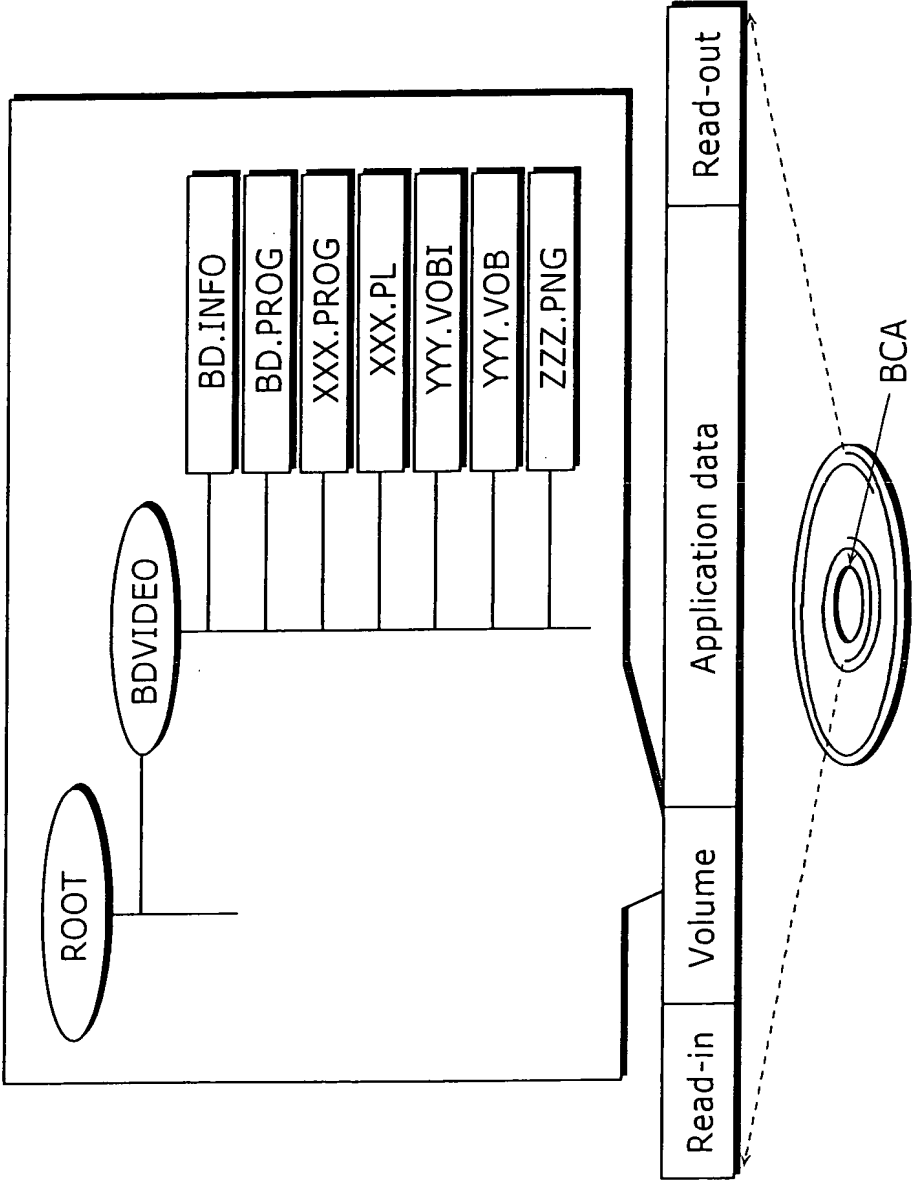


FIG. 24

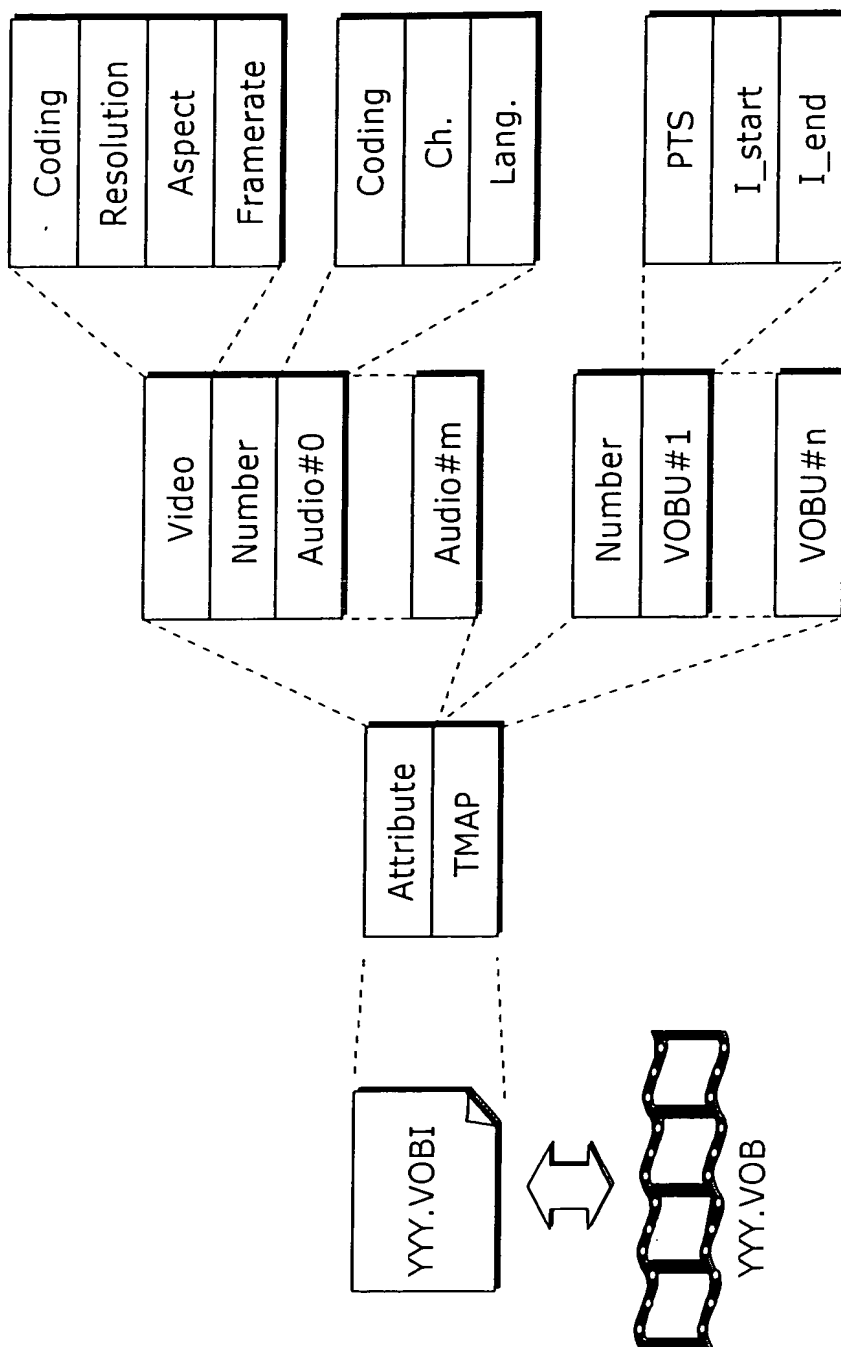


FIG. 25

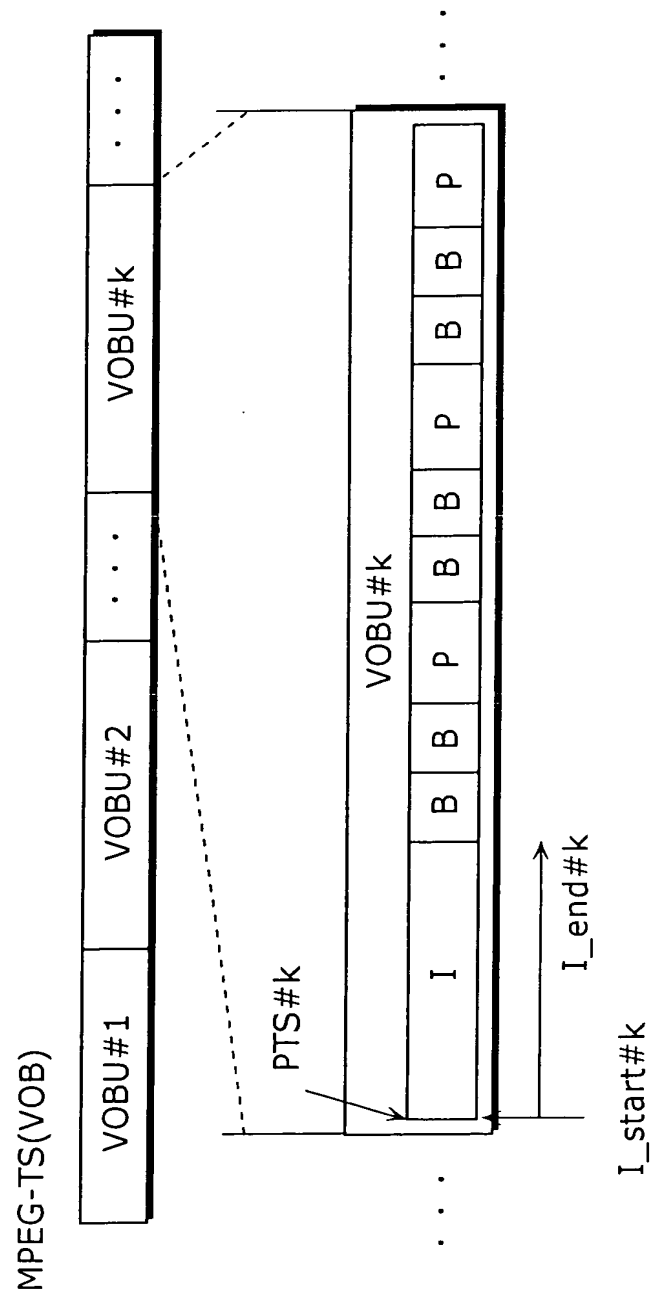


FIG. 26

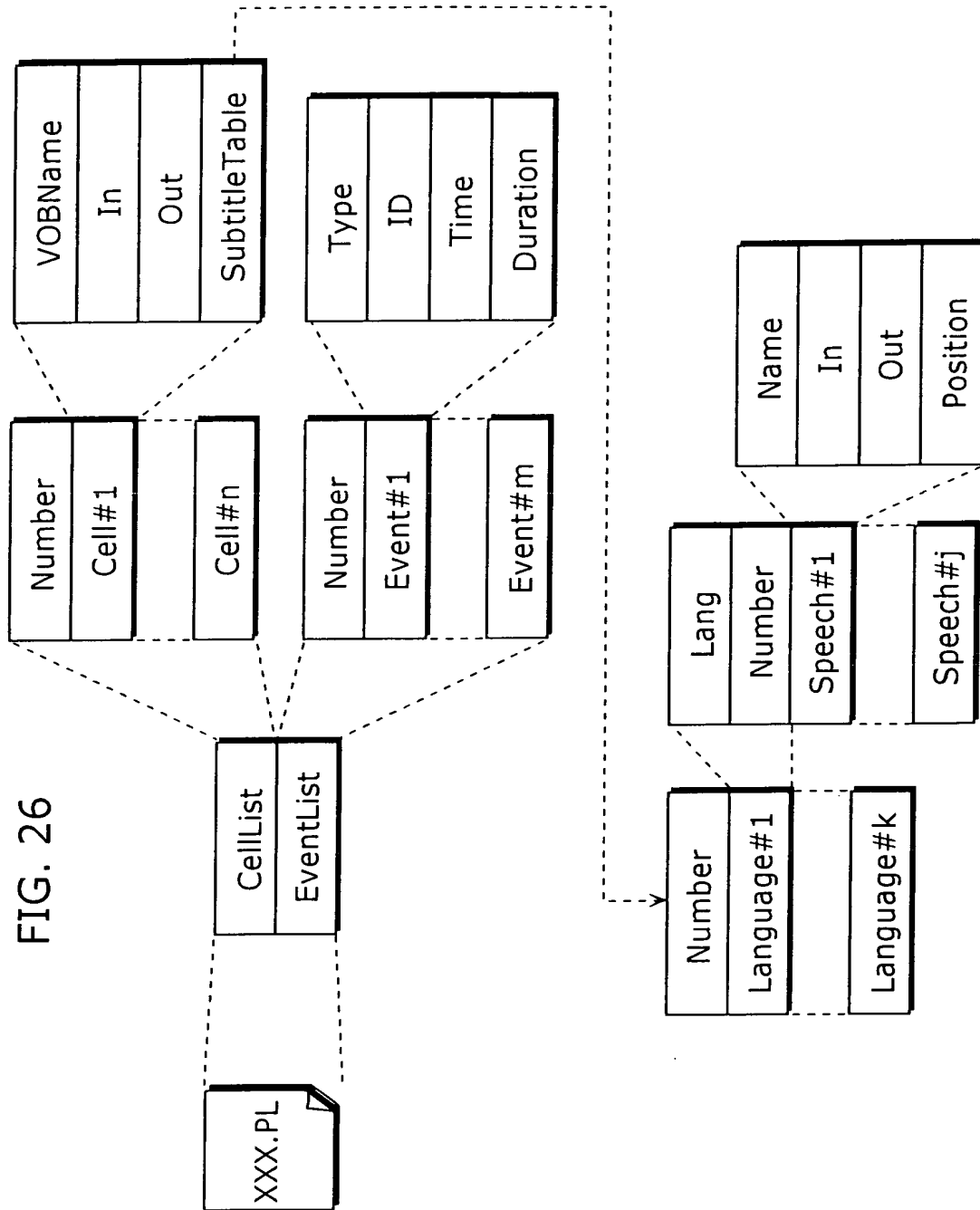


FIG. 27

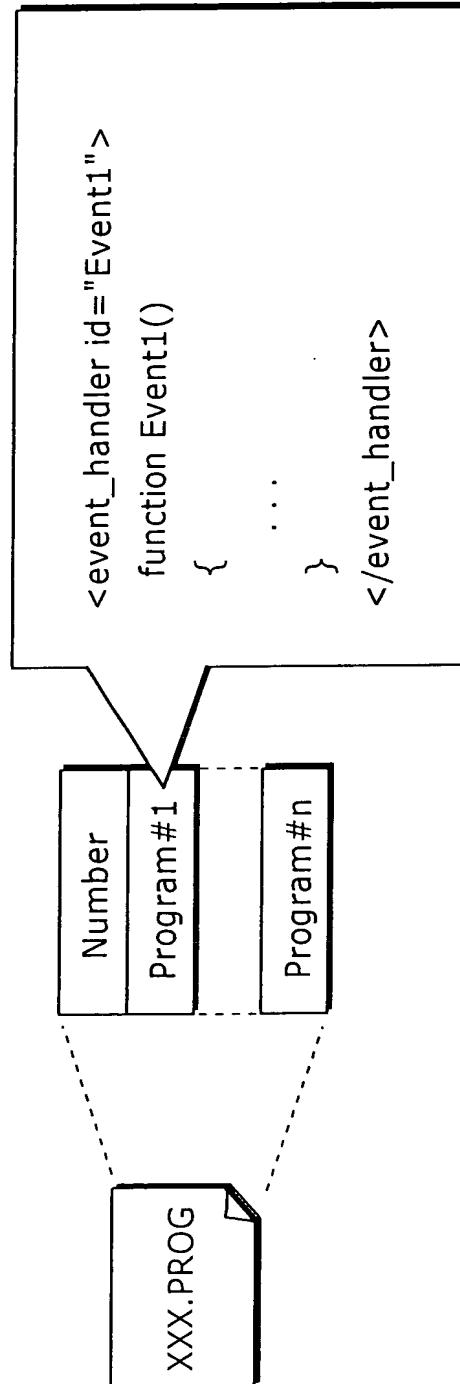


FIG. 28

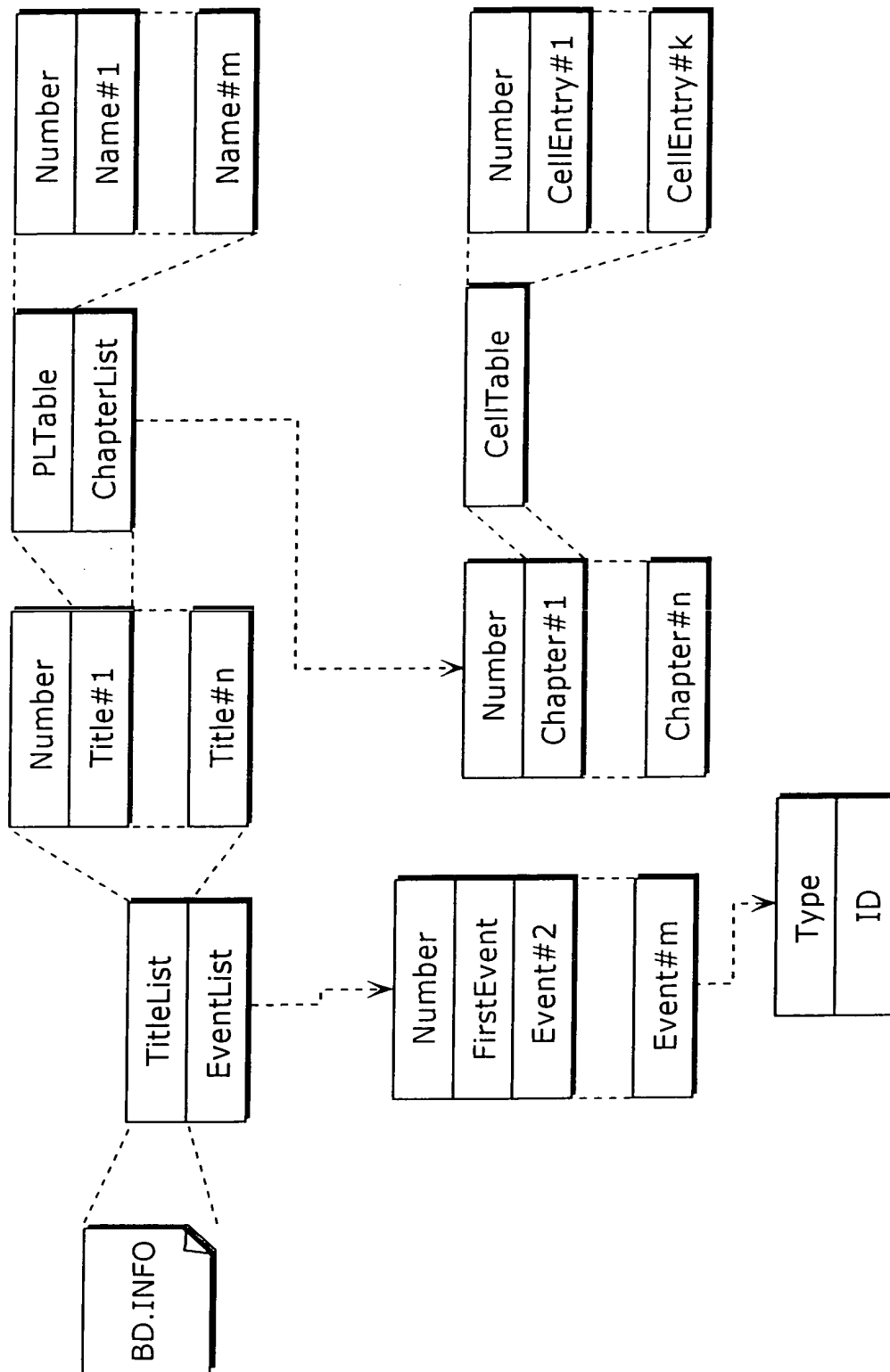


FIG. 29

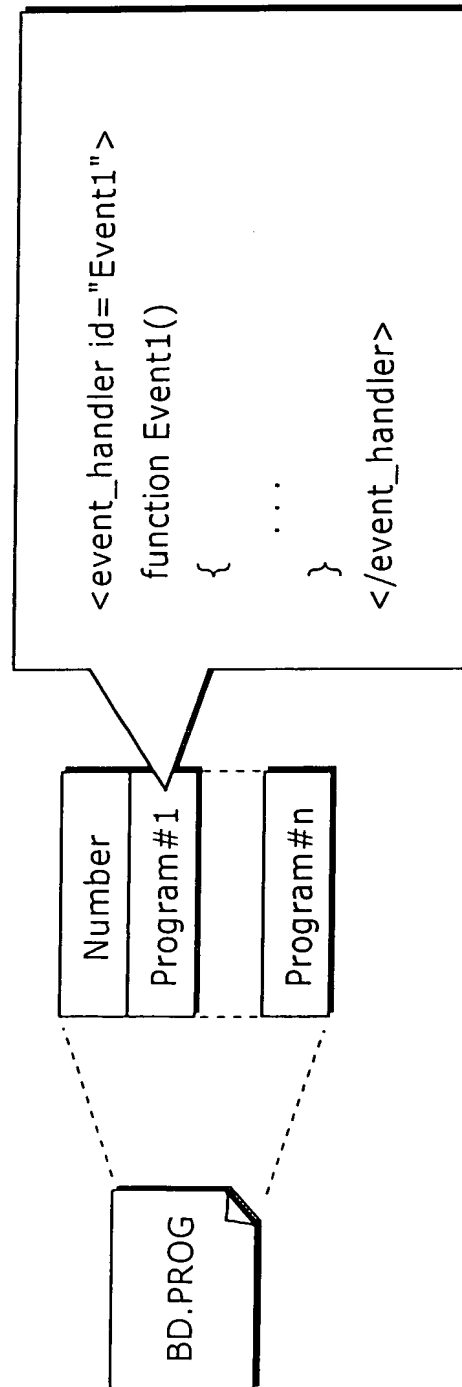


FIG. 30

